

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Mitsunori ISHISAKA, et al.

Serial Number: Not Yet Assigned

(§371 of International Application PCT/JP05/03730)

Filed: August 15, 2006

For: SEMICONDUCTOR MANUFACTURING DEVICE AND METHOD FOR

MANUFACTURING SEMICONDUCTOR DEVICES

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

August 15, 2006

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and

Trademark Office the references listed on the attached PTO-1449 and cited in the enclosed

international search report. References AE-AG are cited in the international search report.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please

charge Deposit Account No. 01-2340.

Respectfully submitted,

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Atty. Docket No. 060471

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		Applicant(s): Mitsunori ISHISAKA, et al.		
ı	PTO-1449	Filing Date: August 15, 2006	Group Art Unit: Not Yet Assigned	

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
	AA						
	AB						
	AC						
	AD						

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)	
/A.C./	AE	7-11446	01/13/95	Japan	Yes-Abstract	
/A.C./	AF	2000-349141	12/15/00	Japan	Yes-Abstract	
/A.C./	AG	9-45624	02/14/97	Japan	Yes-Abstract	
	AH					
	AI					

OTHER DOCUMENTS

	AJ				
	AK				
Examiner	/Ann	a Crowell/ (07/30/2010)	Date Considered	07/30/2010	